

## DEPARTMENT OF COMPUTER SCIENCE & ENGINEERING - CYBER SECURITY

COURSE MODULE OF THE SUBJECT TAUGHT FOR THE SESSION 2025-26 - (EVEN SEM)

Course Syllabus with CO's

Faculty Name/s : Chetan G			Academic Year: 2025- 2026				
Department: CSE-Cyber Security							
Course Code	Course Title	Core/Elective	Prerequisite	Contact Hours			Total Hrs/ Sessions
				L	T	P	
BCO601	Microcontroller & Embedded Systems	Core	Analog and Digital Electronics	4		0	40
<p><b>Course Learning Objectives:</b> This course (BCO601) will enable students to:</p> <ul style="list-style-type: none"> <li>Understand the architectural features and instruction set of 32 bit ARM microcontrollers.</li> <li>Apply instructions of assembly language for programming ARM.</li> <li>Interpret the basic hardware components and their selection method based on the characteristics and attributes of an embedded system.</li> <li>Explain the need of real time operating system for embedded system applications.</li> <li>Develop/test/Conduct the experiments on an ARM7TDMI/LPC2148 evaluation board using Embedded 'C' and Keil Vision tool/Compiler</li> </ul>							
<b>Module 1</b>							<b>Contact Hours</b>
<p>Microprocessors versus Microcontrollers, ARM Embedded Systems: The RISC design philosophy, The ARM Design Philosophy, Embedded System Hardware, Embedded System Software. ARM Processor Fundamentals: Registers, Current Program Status Register, Pipeline, Exceptions, Interrupts, and the Vector Table , Core Extensions</p> <p><b>Text book 1: Chapter 1 - 1.1 to 1.4, Chapter 2 - 2.1 to 2.5 RBT: L1, L2</b></p>							08
<b>Module 2</b>							
<p><b>Introduction to the ARM Instruction Set :</b> Data Processing Instructions , Programme Instructions, Software Interrupt Instruruncions, Program Status Register Instructions, Coprocessor Instructions, Loading Constants</p> <p><b>ARM programming using Assembly language:</b> Writing Assembly code, Profiling and cycle counting, instruction scheduling, Register Allocation, Conditional Execution, Looping Constructs</p> <p><b>Text book 1: Chapter 3:Sections 3.1 to 3.6 ( Excluding 3.5.2), Chapter 6(Sections 6.1 to 6.6) RBT: L1, L2</b></p>							08
<b>Module 3</b>							
<p><b>Embedded System Components:</b> Embedded Vs General computing system, History of embedded systems, Classification of Embedded systems, Major applications areas of embedded systems, purpose of embedded systems</p> <p>Core of an Embedded System including all types of processor/controller, Memory, Sensors, Actuators, LED, 7 segment LED display, stepper motor, Keyboard, Push button switch, Communication Interface (onboard and external types), Embedded firmware, Other system components.</p> <p><b>Text book 2:Chapter 1(Sections 1.2 to 1.6),Chapter 2(Sections 2.1 to 2.6) RBT: L1, L2</b></p>							08
<b>Module 4</b>							
<p><b>Embedded System Design Concepts:</b> Characteristics and Quality Attributes of Embedded Systems, Operational quality attributes ,non-operational quality attributes, Embedded</p>							08

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<p>Systems-Application and Domain specific, Hardware Software Co-Design and Program Modelling, embedded firmware design and development</p> <p><b>Text book 2: Chapter-3, Chapter-4, Chapter-7 (Sections 7.1, 7.2 only), Chapter-9 (Sections 9.1, 9.2, 9.3.1, 9.3.2 only)</b></p> <p><b>RBT: L1, L2</b></p>	
<p><b>Module 5</b></p> <p><b>RTOS and IDE for Embedded System Design:</b> Operating System basics, Types of operating systems, Task, process and threads (Only POSIX Threads with an example program), Thread preemption, Multiprocessing and Multitasking, Task Communication (without any program), Task synchronization issues – Racing and Deadlock, Concept of Binary and counting semaphores (Mutex example without any program), How to choose an RTOS, Integration and testing of Embedded hardware and firmware, Embedded system Development Environment – Block diagram (excluding Keil), Disassembler/decompiler, simulator, emulator and debugging techniques, target hardware debugging, boundary scan.</p> <p><b>Text book 2: Chapter-10 (Sections 10.1, 10.2, 10.3, 10.4 , 10.7, 10.8.1.1, 10.8.1.2, 10.8.2.2, 10.10 only), Chapter 12, Chapter-13 ( block diagram before 13.1, 13.3, 13.4, 13.5, 13.6 only)</b></p> <p><b>RBT: L1, L2</b></p>	08
<p><b>Course Outcomes:</b> The student will be able to :</p> <p><b>CO1:</b> Explain the architectural features and instructions of ARM microcontroller <b>CO2:</b> Apply the knowledge gained for Programming ARM for different applications. <b>CO3:</b> Demonstrate Interfacing of external devices and I/O with ARM microcontroller.</p> <p><b>CO4:</b> Interpret the basic hardware components and their selection method based on the characteristics and attributes of an embedded system.</p> <p><b>CO5:</b> Develop the hardware /software co-design and firmware design approaches</p>	
<p><b>Question Paper Pattern:</b></p> <ul style="list-style-type: none"> <li>• The question paper will have ten questions.</li> <li>• Each full Question consisting of 20 marks</li> <li>• There will be 2 full questions (with a maximum of four sub questions) from each module.</li> <li>• Each full question will have sub questions covering all the topics under a module.</li> <li>• The students will have to answer 5 full questions, selecting one full question from each module.</li> </ul>	
<p><b>Textbooks:</b></p> <ol style="list-style-type: none"> <li>1. Andrew N Sloss, Dominic Symes and Chris Wright, ARM system developers guide, Elsevier, Morgan Kaufman publishers, 2008.</li> <li>2. Shibu K V, “Introduction to Embedded Systems”, Tata McGraw Hill Education, Private Limited, 2<sup>nd</sup> Edition.</li> </ol>	
<p><b>Reference Books:</b></p> <ol style="list-style-type: none"> <li>1. Raghunandan..G.H, Microcontroller (ARM) and Embedded System, Cengage learning Publication, 2019</li> <li>2. The Insider’s Guide to the ARM7 Based Microcontrollers, Hitex Ltd., 1st edition, 2005.</li> <li>3. Steve Furber, ARM System-on-Chip Architecture, Second Edition, Pearson, 2015.</li> <li>4. Raj Kamal, Embedded System, Tata McGraw-Hill Publishers, 2nd Edition, 2008.</li> </ol>	
<p><b>Internal Assessment Marks:</b> 50 (CIE for theory component-25 Marks: 2 Tests, each of 15 marks and other assessments for 10 marks and CIE for Practical component-25 Marks: conduction of the experiment along with laboratory record for 15 Marks and test for 10 Marks).</p>	



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### The Correlation of Course Outcomes (CO's) and Program Outcomes (PO's)

Subject with Subject Code:		Microcontroller & Embedded Systems- BC0601											Faculty Name: Chetan G					
		Program Outcomes											Program Specific Outcomes					
	RBT	PO1	PO2	PO3	PO4	PO5	PO6	PO7	PO8	PO9	PO10	PO11	PO12	Total	PSO1	PSO2	PSO3	Total
CO1	L2			1	-	-	-	-	-	-	-	-	-	1				-
CO2	L3			2	-	-	-	-	-	-	-	-	-	2				-
CO3	L3		2		-	-	-	-	-	-	-	-	-	2	1			1
CO4	L3			3	-	-	-	-	-	-	-	-	-	3	2			2
CO5	L3			3	-	-	-	-	-	-	-	-	-	3				-
<b>Total</b>			<b>2</b>	<b>9</b>										<b>11</b>	<b>3</b>			<b>3</b>





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