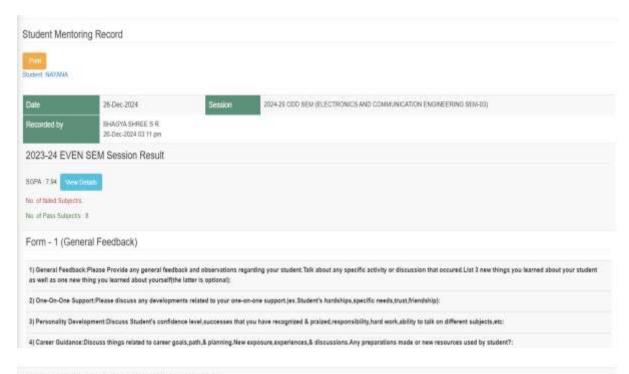
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PMATERIA	Mathematics II for EEE Streets			3-3-2	2-2-3	Prof. Hangamanny S			RS
THE PERSON NAMED IN	IZ Aggisted Physics			3.53	2-3-3	Prof. Anjan Kamar J			AJ .
and the latest designation of the latest des	HEE PAR Shows of Electronics			334	2/3/0	Prof. Specribi P S			1975
	2001 Detrofiction to J. Programming			244	2302	Prof. Shruthi H G			WG.
	TO \$2015 Research Energy Sources			5-0-0	3988	Dr.Saitch K. R.Dr. Pravens Kamar M.			SKRPN
	WIND Development of Street of Strings (SIX)			3-0-0	344	Prof. Specritis F N			525
	EXCESSED Introduction to Coher Servery			244	3-0-8	Prof. Marta Suchana, Frof. Modharya B. Kohmor			MS,MBE
	Die Frederickeel Wetting Skille			164	350-00	Prof. Blocathi B			1684
HILDRICH ST	The state of the s			160	1:0:0	Prof. Lesbrothi C.R.			CHE
	Interaction and Swiger Phinking			1.60	3-0-0	Amitha S			15
	eta hours ner ter s NB		HOB OF	hics Physica Donesis and	Asst. De	an Acad		DELICATION OF SAME	Aleks Salahan

Figure 9.1.1 Timetable displaying mentoring hour.



NO OF UNITS COMPLETED IN EACH SUBJECT No. of Units Remedial Class required or Difficulties faced by the student in understanding Suggestion by Completed the subject Mentor 1 BMATECIOS-MATHEMATICS-II FOR EC MACHUSUDHANKV 5 No 2 BEC002-DIGITAL SYSTEM DESIGN USING PRAGEER KUMAR Y 5 **VERILOG** 3 BEC303-ELECTRONIC PRINCIPLES AND CIRCUITS MYTHRIR 4 BEC304-NETWORK ANALYSIS VEERAPRATHAP V 5 No 5 HECL305-ANALOG AND DIGITAL SYSTEMS CHANDRA SHEKAR 8 DESIGN LAB 6 BEC395-COMPUTER ORGANIZATION AND RAJEEV GOWDAR 5 No ARCHITECTURE 7 8SCK307-SGCIAL CONNECT AND SHALINI HANOK 2 RESPONSIBILITY 8 BEC358A-LABVEW PROGRAMMING GIRISH M. 12

ATTENANCE STATUS IN EACH SUBJECT

5.No.	Subject	Faculty	Amendance %	Reason for Shortage of attendance	Suggestions	Parent intervention required or not
.1	BMATEC301-MATHEMATICS III FOR EC ENGINEERING.	MACHUSUDHAN K V	85.71			
2	BEC302-DIGITAL SYSTEM DESIGN USING VERILOG	PRADEEP KUMAR Y	94.64			
3	BEC303-ELECTRONIC PRINCIPLES AND CIRCUITS	MYTHRIR.	92.06			
4.	BEC304-NETWORK ANALYSIS	VEERAPRATHAP V	83.64			
5	BECLISS ANALOG AND DIGITAL SYSTEMS DESIGN LAB	CHANDRA SHEKAR P	92.31			
83	BEC306-COMPUTER ORGANIZATION AND ARCHITECTURE	RAJEEV GOWDAR	90.63			
7	BSCK307-SOCIAL CONNECT AND RESPONSIBILITY	SHALINI HANCK	100.00			
8	BEC3S8A-LABVIEW PROGRAMMING	GIRISH M	91.67			

Figure 9.1.2 Format adopted for mentoring

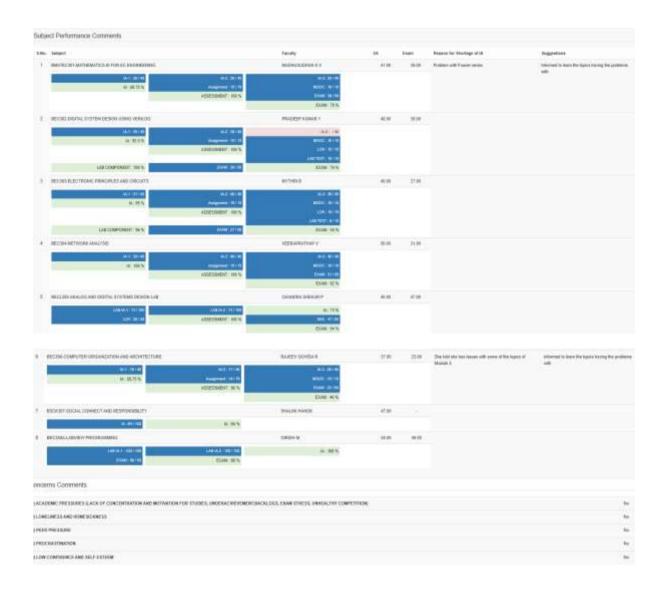


Figure 9.1.2 Format adopted for mentoring



NPTEL Online Certification (Funded by the MoE, Govt. of India)



This certificate is awarded to

CHETHAN M C

for successfully completing the course

VLSI Design Flow: RTL to GDS

with a consolidated score of

53

%

Online Assignments | 19.38/25 | Proctored Exam

33.75/75

Total number of candidates certified in this course: 1877

Dr. Anand Srivastava

Continued Education Program, IIITD

Jul-Oct 2023

(12 week course)

Prof. Andrew Thangaraj NPTEL, Coordinator IIT Madras



INDRAPRASTHA INSTITUTE of INFORMATION TECHNOLOGY DELHI

Roll No: NPTEL23EE137S742500190

To verify the certificate

No. of credits recommended: 3 or 4

9.1.3: Sample of the certificate by the student completed online course from NPTEL.